ABSTRACT

The present invention relates to a microcomputer that has fewer terminals that are necessary only for debugging, such as a terminal for inputting a forced break on a mass-produced chip, and electronic equipment and a debugging system that comprise the same. In a microcomputer (10) having a user mode and a debugging mode, an SIOD (16) functions as a terminal for inputting a signal for a forced break when in user mode, and it functions as a terminal for communicating debugging information when in debugging mode. When an external debugging tool (14) is not connected, the SIOD (16) is pulled up and held at high level, and the configuration is such that it can be set to any level (high or low) by connecting it to a debugging tool (14). The run mode is determined at a time of reset based on whether the SIOD (16) is high or low.